

Defect And Fault Tolerance In VLSI Systems: Volume 2

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Proc. 1990 International Workshop on Defect and Fault Tolerance in VLSI Systems, Grenoble, France, Nov. 1990. June 1994, Vol. 1, pp. 333 338.

Dft abbreviation stands for defect and fault

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Gabriele saucier (editor of defect and fault

Gabriele Saucier is the author of Logic And Architecture Synthesis Fast Prototyping Of Vlsi: Defect and Fault Tolerance in VLSI Systems: Volume 2 by C H

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Yield and reliability of vlsi circuits

List of Yield and Reliability Related Circuits," Defect and Fault Tolerance in VLSI Systems, Vol. 2, on Defect and Fault Tolerance in VLSI Systems,

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Marco [defect and fault tolerance group]

Marco Ottavi is currently an Associate Professor at the University of Rome Tor Vergata , before he was a Research Professor with the same University as the

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