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Study of gate oxide breakdown caused by charge

Study of gate oxide breakdown caused by charge buildup during dry etching: Authors: Nojiri, Kazuo; Journal of Vacuum Science & Technology B:

Patent us5952245 - method for processing samples -

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Dry etching | refurbished, surplus, used etch

AG Semiconductor provides Used, Available technologies consist of physical vapor deposition both wet etching and dry etching such as reactive ion etch

Etching by m = 0 helicon plasma - references -

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