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Dry etching | refurbished, surplus, used etch

AG Semiconductor provides Used, Available technologies consist of physical vapor deposition both wet etching and dry etching such as reactive ion etch

Nanoplas develops dry-etching technology for

Nanoplas, a global supplier of plasma processing equipment to the semiconductor industry, announces a new dry-etch process offering etch selectivity for removing

Patent us4855017 - trench etch process for a

Dry etching process for semiconductor: US5888414 Micron Technology, Inc. Semiconductor processing method of making electrical Kazuo Takata: Etching method and

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Autor. Kazuo Nojiri is a CTO of Lam Research Japan. He has 37 years of experience in semiconductor industry. Prior to joining Lam in 2000, he worked for Hitachi Ltd

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Dry etching refers to the removal of Dry etching is currently used in semiconductor fabrication processes due to its unique ability over wet etch to do

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Mashimo S and Tsukada T 1993 Proc. Symp. on High Selective Dry Etching and Nojiri K and Tsunokuni K 1993 Pulsed plasma etching for semiconductor

Kazuo nojiri (author of dry etching technology

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Uno shoichi filed applications and granted patents

List of filed applications and granted patents for UNO SHOICHI. Authors: YUNOGAMI TAKASHI, NOJIRI KAZUO, MAEKAWA ATSUSHI, UNO SHOICHI, a plasma dry etching

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Etching media and technology ("dry"). Each of these exists in several varieties. Wet etching Semiconductor technology;

Brevetto us6186153 - plasma treatment method and

Shigeru Kakuta, Kazuo Nojiri, Kazue Takahashi: Assegnatario originale: including dry etching technology using Plasma dry cleaning of semiconductor

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Patent us5952245 - method for processing samples -

Kazuo Nojiri , Yoshinao Kawasaki Dry-etching apparatus: Integrated dry-wet processing apparatus and method for removing material on semiconductor wafers

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