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References from the article Developments of Plasma Etching Technology for Fabricating Semiconductor Dry Etching Technology Nojiri 1999 Proc. 21st Symp. Dry

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## **Etching by $m = 0$ helicon plasma - references -**

Mashimo S and Tsukada T 1993 Proc. Symp. on High Selective Dry Etching and Nojiri K and Tsunokuni K 1993 Pulsed plasma etching for semiconductor

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Shigeru Kakuta, Kazuo Nojiri, Kazue Takahashi: Assegnatario originale: including dry etching technology using Plasma dry cleaning of semiconductor

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Study of gate oxide breakdown caused by charge buildup during dry etching: Authors: Nojiri, Kazuo; Journal of Vacuum Science & Technology B:

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Kazuo Nojiri , Yoshinao Kawasaki Dry-etching apparatus: Integrated dry-wet processing apparatus and method for removing material on semiconductor wafers

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Dry etching refers to the removal of Dry etching is currently used in semiconductor fabrication processes due to its unique ability over wet etch to do

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%0 Journal Article %A Nojiri, Kazuo %A Tsunokuni, Kazuyuki %T Study of gate oxide breakdown caused by charge buildup during dry etching %D 1993 %J Journal of Vacuum

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Autor. Kazuo Nojiri is a CTO of Lam Research Japan. He has 37 years of experience in semiconductor industry. Prior to joining Lam in 2000, he worked for Hitachi Ltd

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AG Semiconductor provides Used, Available technologies consist of physical vapor deposition both wet etching and dry etching such as reactive ion etch

### **Research books: physics/electromagnetism/**

Books: Physics: Electromagnetism: Semiconductor Physics. Series on Semiconductor Science & Technology); Kazuo Nojiri (2014) Dry Etching Technology for

### **Uno shoichi filed applications and granted patents**

List of filed applications and granted patents for UNO SHOICHI. Authors: YUNOGAMI TAKASHI, NOJIRI KAZUO, MAEKAWA ATSUSHI, UNO SHOICHI, a plasma dry etching

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